

Fig. 1

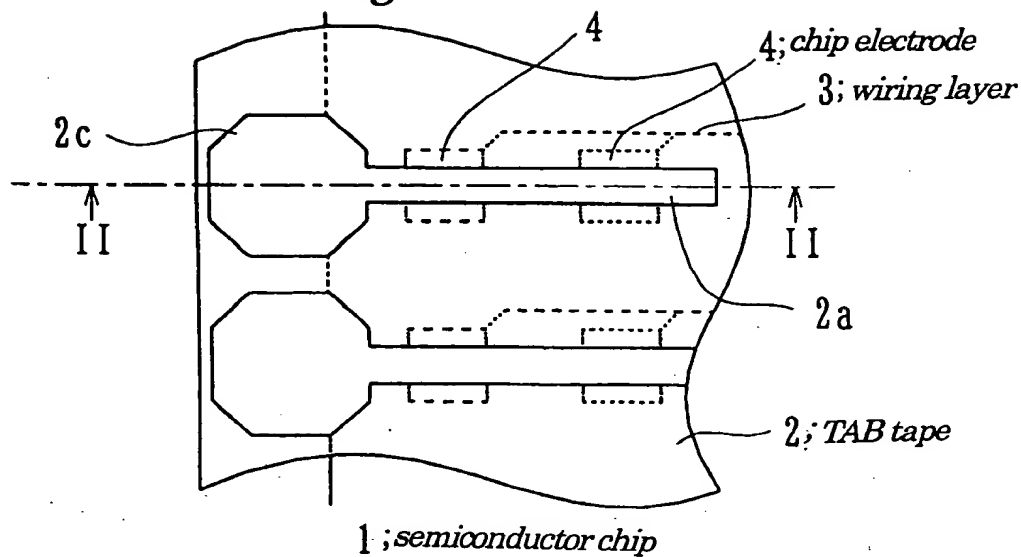


Fig. 2(a)

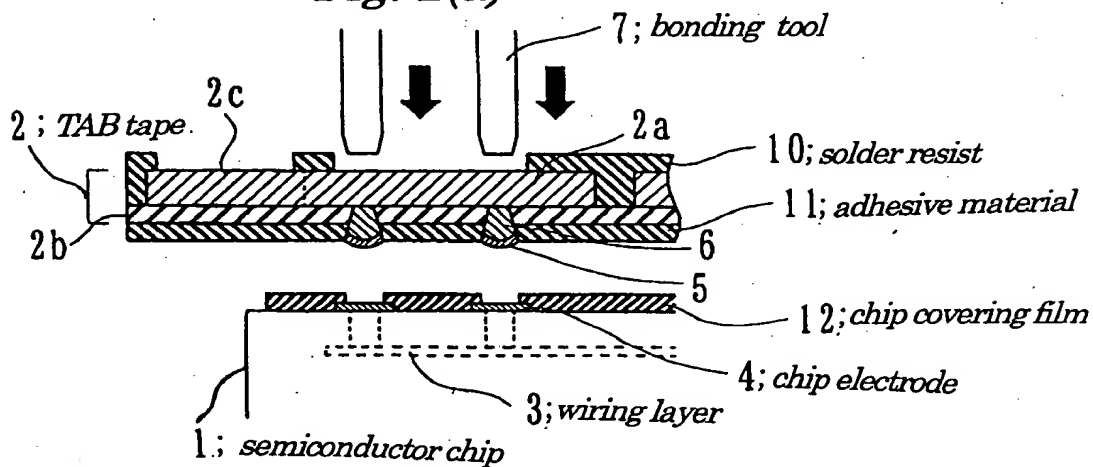
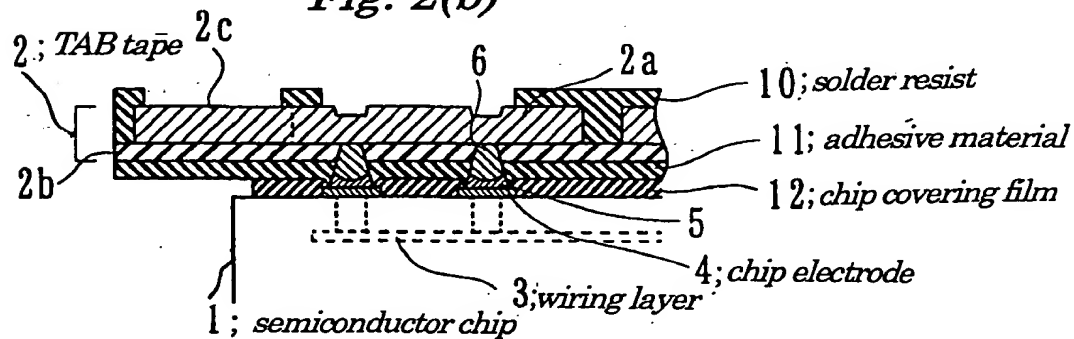


Fig. 2(b)



1; semiconductor chip *Fig. 3*

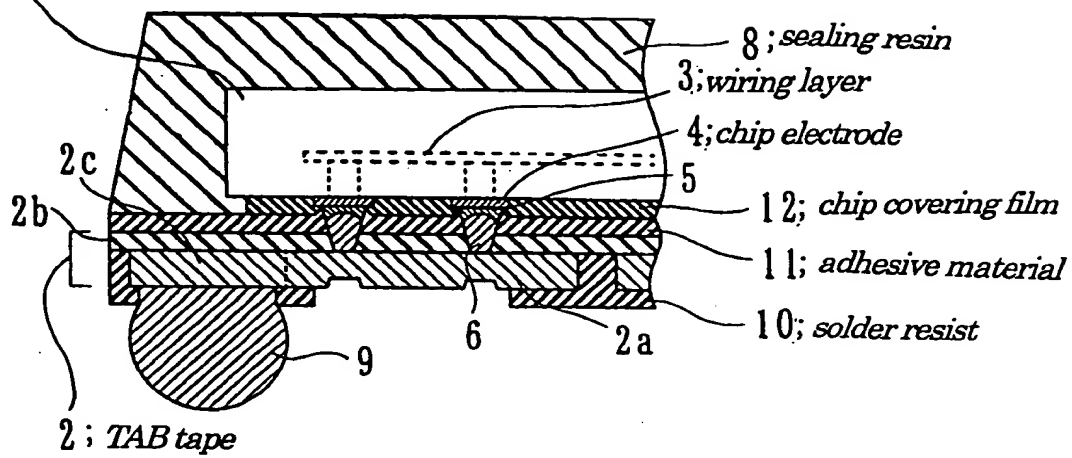


Fig. 4

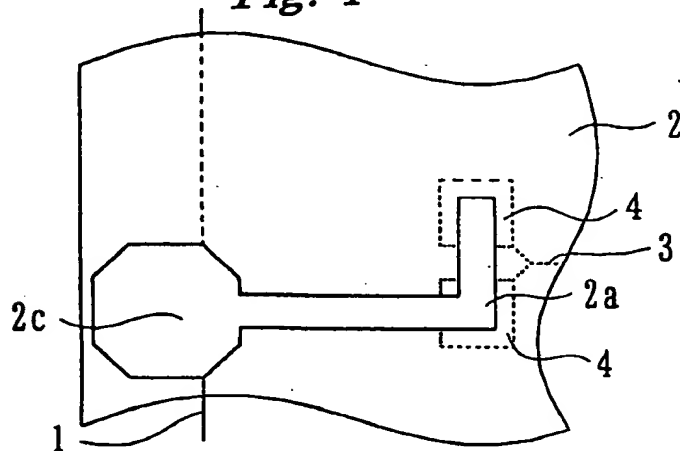


Fig. 5

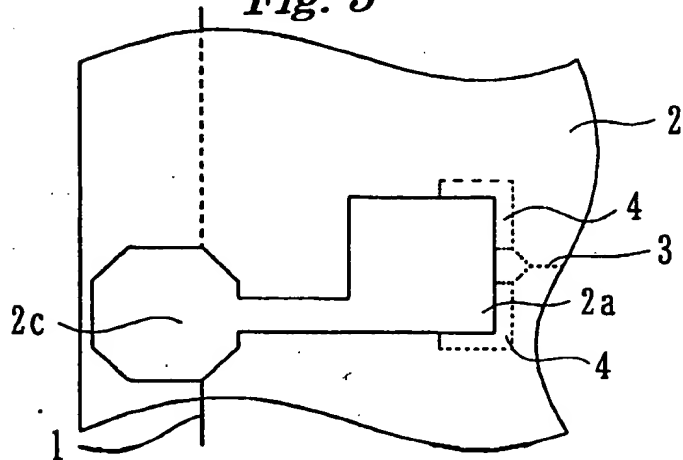


Fig. 6

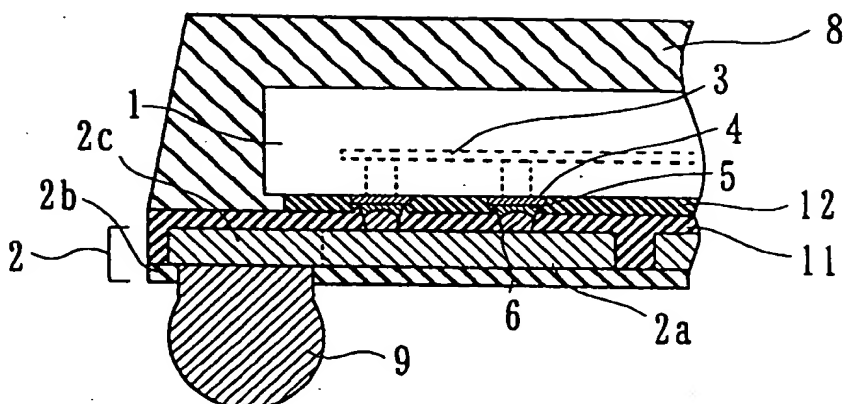


Fig. 7 (PRIOR ART)

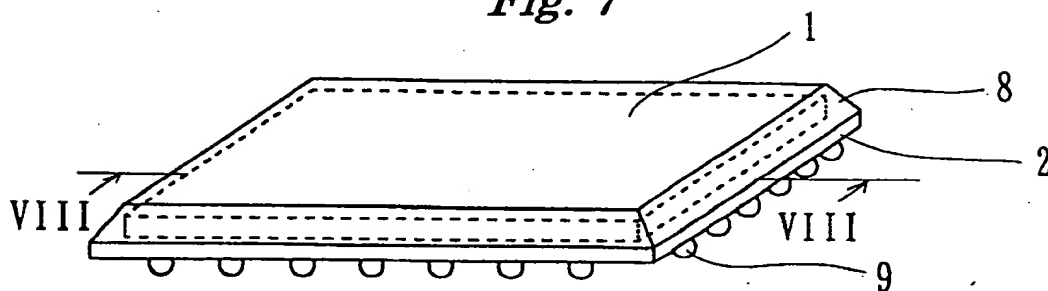


Fig. 8 (PRIOR ART)

